

<b>Interview Summary</b>	Application No. 09/899,915	Applicant(s) TAM, SIMON	
	Examiner Regina Liang	Art Unit 2674	

All participants (applicant, applicant's representative, PTO personnel):

- (1) Regina Liang. (3) Mr. Suzuki.  
 (2) Holly Moore (50212). (4) \_\_\_\_\_.

Date of Interview: 15 February 2005.

Type: a) ☐ Telephonic b) ☐ Video Conference  
 c) ☒ Personal [copy given to: 1) ☐ applicant 2) ☐ applicant's representative]

Exhibit shown or demonstration conducted: d) ☐ Yes e) ☒ No.  
 If Yes, brief description: \_\_\_\_\_.

Claim(s) discussed: \_\_\_\_\_.

Identification of prior art discussed: Dawson et al.

Agreement with respect to the claims f) ☐ was reached. g) ☒ was not reached. h) ☐ N/A.

Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments: Attorney explaining the invention and the proposed claims, we discussed the difference between the reference and the proposed claims. Attorney will define more detail in the programming stage to include the current path connecting a data line, which seems to be Dawson reference, the examiner will reconsider upon receiving the formal response.  
*overcome*

(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)

THE FORMAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MPEP Section 713.04). If a reply to the last Office action has already been filed, APPLICANT IS GIVEN ONE MONTH FROM THIS INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW SUMMARY FORM, WHICHEVER IS LATER, TO FILE A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW. See Summary of Record of Interview requirements on reverse side or on attached sheet.

Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.

  
 Examiner's signature, if required

**OLIFF & BERRIDGE, PLC**

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February 10, 2005

**FACSIMILE TRANSMISSION COVER SHEET**

To: Examiner R. Liang  
Group Art Unit: 2674  
Phone No.: (703) 305-4719  
Facsimile No.: (703) 746-5996

From: Holly N. Moore; Reg. No. 50,212Your Ref.: Application No. 09/899,915 Our Ref.: 110032Number of Pages Sent (Including cover sheet): 5Prepared By: ale**Comments:**

Examiner Liang:

Please find attached an Appendix for discussion purposes only during the personal interview scheduled on February 15, 2005 for the above-identified application. Please call me if you have any questions.

Very truly,

Holly N. Moore

Sent by: ale

This facsimile is intended only for the use of the individual or entity named above and may contain privileged or confidential information. If you are not the intended recipient, or the employee or agent responsible to deliver it to the intended recipient, you are notified that any review, dissemination, distribution or copying of this facsimile is prohibited. If you have received this facsimile in error, please immediately notify us by facsimile or telephone, and return the facsimile to us by mail at the above address.

### Applicant Initiated Interview Request Form

Application No.: 09/899,915 First Named Applicant: Simon TAM  
 Examiner: R. Liang Art Unit: 2674 Status of Application: Pending

**Tentative Participants:**

(1) Examiner Liang (2) Holly N. Moore  
 (3) Mr. Suzuki (4) \_\_\_\_\_

Proposed Date of Interview: February 15, 2005 Proposed Time: 10:00 (AM)

**Type of Interview Requested:**

(1) ☐ Telephonic (2) ☒ Personal (3) ☐ Video Conference

Exhibit To Be Shown or Demonstrated: ☐ YES ☒ NO  
 If yes, provide brief description:

### Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) Rejection	4, 11-12, 27, 32 and 34	Dawson et al. and Bae et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(2) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(3) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(4) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Continuation Sheet Attached

**Brief Description of Arguments to be Presented:**

(1) Detailed explanation of application; (2) review features of independent claims 4, 11-12, 27, 32 and 34  
 (3) reviewing proposed independent claims regarding electro-optical device, driver circuit and method  
 for controlling current; and (4) discussion of how independent claims distinguish over Dawson and Bae.

An interview was conducted on the above-identified application on \_\_\_\_\_

**NOTE:**

This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of substance of this interview (37 CFR 1.133(b)) as soon as possible.

\_\_\_\_\_  
 (Applicant/Applicant's Representative Signature)

\_\_\_\_\_  
 (Examiner/SPE Signature)

## APPENDIX

4. (Currently Amended) A driver circuit to drive a pixel of an electroluminescent device, the pixel including an electroluminescent element, the circuit comprising:

a transistor ~~connected so as to operatively control a~~ of which a conducting state is set according to a data current that determines a current level of a driving current supplied to the electroluminescent element;

a first switching device connected so as to establish a first current path through which ~~a~~ the data current flows during a programming stage, the data current flowing through the transistor during the programming stage; and

a second switching device connected so as to establish a second current path through the transistor and the electroluminescent element during a reproduction stage,

the first switching device being connected such that the first current path does not pass through the electroluminescent element during the programming stage, and

the first and second switching devices being controlled by respective control signals supplied from separate signal lines.

11. (Currently Amended) A method of controlling a ~~current supply~~ of a driving current to an electroluminescent element, the method comprising:

providing a first current path through which a data current that determines a current level of the driving current flows during a programming stage by using a first switching device connected so as to establish the first current path, said first current path not passing through the electroluminescent element; and

providing a second current path during a reproduction stage by using controlling a second switching device connected so as to establish the second current path, ~~said~~ the second current path passing through the electroluminescent element,

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the first switching device and the second switching device being controlled by respective control signals supplied from separate signal lines.

12. (Currently Amended) A method of controlling a current supply to an electroluminescent element, the method comprising:

providing a first current path including a transistor during a programming stage, said current path connecting to a current sink through a data line; and

providing a second current path including the transistor during a reproduction stage, said the second current path passing through the electroluminescent element,

providing a data current that determines a conduction state of the transistor, the data current flowing from a power-supply line to a data line through the first current path,

supplying a driving current to the electroluminescent element, the driving current flowing through the second current path, and

providing a current level of the driving current corresponding to the conduction state of the transistor.

27. (Currently Amended) A circuit comprising a current driven element,

the circuit providing a first current path flowing through which a data current that determines a current level of a driving current supplied to the current driven element flows by controlling a first switching means, the data current not flowing through the current driven element,

the circuit providing a second current path flowing through which the driving current through the current driven element flows by controlling a second switching means, the driving current flows through the current driven element, and

the first switching means and the second switching means being controlled by respective control signals supplied from separate signal lines.

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32. (Currently Amended) An electro-optical device ~~having comprising~~ a plurality of pixels, each of the plurality of pixels ~~comprising including~~ a circuit ~~with a current driven element and a current determining device that determines a current according to a data signal, that controls a driving current supplied to a current driven element,~~

the circuit providing a first current path that excludes the current driven element by controlling a first switching device, a data current that determines a current level of the driving current flowing through the first current path,

\_\_\_\_\_ the circuit further providing a second current path ~~that~~ includes the current driven element by controlling a second switching device, and

the first and second switching devices being controlled by respective control signals supplied from separate signal lines.

34. (Currently Amended) A circuit comprising a current driven element, the circuit providing:

\_\_\_\_\_ a first current path ~~including the current driven element during a first period~~ and a second current path ~~not including the current driven element, the second current path being connected to a current sink through a data line during a programming stage during a second period,~~

\_\_\_\_\_ a data current that determines a conduction state of a transistor included in the circuit, the data current flowing through the first current path,

\_\_\_\_\_ a driving current of which a current level corresponds to the conduction state of the transistor, the driving current flowing through the second current path,

\_\_\_\_\_ the data current flowing to a current sink through the transistor, and

\_\_\_\_\_ the driving current flowing to the current driven element through the transistor.